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MICROCONTROLLER BASED DIGITAL CLOCK This Project Report Is Submitted To The Department Of Electrical & Electronic Engineering (EEE), Daffodil International University, Bangladesh, In Partial Fulfillment Of The Requirements For The Degree Of “Bachelor Of Science In Electrical & Electronic Engineering”. Supervised By Md. Mahmudur Rahman Senior Lecturer Department Of Electrical & Electron Jul 18th, 2020

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Figure 1: RTL Schematic Diagram Of The Digital Clock VHDL File The Digital\_clock Block Has 6 Inputs Clock Clk, Reset, Incr\_min, Incr\_min1, Incr\_hour And Incr\_hour1. It Has 2 Outputs One Is “mux” And The Other “output.” Inside The Whole Block There Is 6 Blocks And Each Of Them Are Four Different Functions. The Clk1Hz Block Makes The VHDL File Works According To The Time. The Counter ... Oct 25th, 2020

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Electrical And Computer Engineering Department  
School Of Engineering And Computer Science Oakland  
University, Rochester, MI E-mails:

Rnvarma@oakland.edu, Mmpriedi@oakland.edu

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